

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Jerimy Nelson, et al.

Confirmation No.:

Application No.:

Examiner:

Filing Date:

Group Art Unit:

Title: SYSTEM AND METHOD FOR POPULATING A COMPUTER-AIDED DESIGN PROGRAM'S
DATABASE WITH DESIGN PARAMETERS

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of national application; or before
mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97(c) together with either a:
☐ Statement under 37 CFR 1.97(e), or
☐ a \$180.00 fee under 37 CFR 1.17(p), or
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97 (d) together with a:
☐ Statement under 37 CFR 1.97(e)(1) or (2), and
☐ a \$180.00 fee set forth in 37 CFR 1.17(p).
(Filed after final action, a notice of allowance, on or before payment of the issue fee)

Please charge to Deposit Account **08-2025** the sum of \$0.00 . At any time during the
pendency of this application, please charge any fees required or credit any overpayment to Deposit
Account **08-2025** pursuant to 37 CFR 1.25.

☒ Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Statement together with
any required copies of patents, publications or other information of which applicant(s) are aware, which
applicant(s) believe(s) may be material to the examination of this application and for which there may be
a duty to disclose in accordance with 37 CFR 1.56.

☐ A concise explanation of the relevance of foreign language patents, foreign language publications
and other foreign language information listed on PTO Form 1449, as presently understood by the
individual(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the
attached sheet, or where a foreign language patent is cited in a search report or other action by a
foreign patent office in a counterpart foreign application, an English language version of the search
report or action which indicates the degree of relevance found by the foreign office is listed on form PTO
1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

"Express Mail" label no. **EV256030349US**

Date of Deposit **12-4-2003**

I hereby certify that this is being deposited with the
United States Postal Service "Express Mail Post
Office to Addressee" service under 37 CFR 1.10 on
the date indicated above and is addressed to:
Commissioner for Patents, Alexandria, VA
22313-1450.

By

Typed Name: John Pallivathukal

Respectfully submitted,

Jerimy Nelson, et al.

By

Jody C. Bishop

Attorney/Agent for Applicant(s)
Reg. No. **44,034**

Date: **05/14/1996**

PATENT APPLICATION

Sheet 1 of 4

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
	200209358-1		
	APPLICANT		
	Jerimy Nelson, et al.		
	FILING DATE	GROUP	

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
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	1B	5,623,417	04/22/1997	Iwasaki et al.	
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	1E	5,655,109	08/05/1997	Hamid	
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	1G	5,712,794	01/27/1998	Hong	
	1H	5,721,813	02/24/1998	Leinhos	
	1I	5,754,738	05/19/1998	Saucedo et al.	
	1J	5,757,655	05/26/1998	Shih et al.	
	1K	5,828,580	10/27/1998	Ho	

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		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1Q	U.S. Patent Application Publication No. 2002/0023110, published 02/21/2002, Fortin et al.
1R	Cadence PCB, "Allegro Product Family," Internet, http://www.cadencepcb.com/products/allegro/family.asp , 10/16/2003, Pages 1-2.
1S	Cadence PCB, Allegro Product Home Page, "Allegro High-speed PCB layout, routing, and manufacturing output," Internet, http://www.cadencepcb.com/products/allegro , 10/16/2003, Page 1.

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	2G	6,499,135	12/24/2002	Li et al.	
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	2I				
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	2Q	PC Engines™, Programmable Keypad for Cadence Allegro™," Internet http://www.pcengines.ch/keypad.htm , 10/16/2003, page 1.
	2R	Cadence PCB, Allegro: Product Integration, Products, "Allegro Integration," Internet http://www.cadencepcb.com/products/allegro/integration.asp , 10/16/2003, pages 1-2.
	2S	Cadence PCB, Allegro: Top Benefits, Products, "ALLEGRO Top Benefits," Internet http://www.cadencepcb.com/products/allegro/topbenefits.asp , 10/16/2003, pages 1-2.

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Sheet 4 of 4

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 200209358-1	APPLICATION NO.	CONFIRMATION NO.
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4Q	Cadence, "Advanced Package Designer QuickStart Service."
4R	Cadence PCB, PCB Design Studio: Datasheet, "PCB DESIGN STUDIO Datasheet," Internet http://www.cadencepcb.com/products/studiosuite/datasheets/default.asp , 10/16/2003, pages 1-9.
4S	Cadence, datasheet, "Advanced Package Designer," Candence's® Advanced Package Designer System, Industry Overview, Copyright © 1998. Cadence Design Systems, Inc. Marketing Services, pages 1-2.

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